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APPLICANTS

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** CONTINUING DATA *****

This application is a CIP of 10/410,206 04/10/2003 ABN
yes

** FOREIGN APPLICATIONS *****

JAPAN 2002-318078(P) 10/31/2002

JAPAN 2003-276468(P) 07/18/2003
yes

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	JAPAN	41	31	3

ADDRESS

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TITLE

Semiconductor device and semiconductor memory device provided with internal current setting adjustment circuit

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